

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
BIT.USPT.	238397
BITS.USPT.	164815
DATA.USPT.	727227
DATUM.USPT.	13486
LINE\$1	0
LINE.USPT.	1604802
LINEA.USPT.	207
LINEB.USPT.	5
LINEC.USPT.	7
LINED.USPT.	61686
LINEE.USPT.	31
(L1 AND (BIT OR DATA) ADJ LINE\$1).USPT.	3277

[There are more results than shown above. Click here to view the entire set.](#)

US Patents Full-Text Database

US Pre-Grant Publication Full-Text Database

JPO Abstracts Database

EPO Abstracts Database

Derwent World Patents Index

IBM Technical Disclosure Bulletins

Database:

Search:

L2

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History**

DATE: Wednesday, November 06, 2002 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

*DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*L16 compressed adj analog adj signal 99 L16L15 bus adj decompress\$4 and analog adj signal\$1 16 L15*DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ*L14 bus adj compress\$4 same analog adj (out\$4 or signal\$1) 3 L14*DB=USPT; PLUR=YES; OP=ADJ*L13 bus adj compress\$4 same analog adj (out\$4 or signal\$1) 1 L13L12 bus adj compress\$4 and analog adj (out\$4 or signal\$1) 18 L12L11 bus adj compress\$4 and analog adj (out\$4 or signal\$1) 18 L11L10 bus adj compress\$4 and analog 38 L10L9 L5 and analog adj signal 18 L9L8 L7 and analog adj signal 1 L8L7 L6 and adder 1 L7L6 L5 and voltage adj control\$3 7 L6L5 bus adj compress\$4 98 L5L4 L3 and analog adj signal 1 L4L3 L1 and adder 1 L3L2 L1 and voltage adj control\$3 1 L2L1 bus adj compress\$4 and bus adj decompress\$4 11 L1

END OF SEARCH HISTORY

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L12</u>	L9 and level adj detect\$3	1	<u>L12</u>
<u>L11</u>	L9 and output adj signal\$1	7	<u>L11</u>
<u>L10</u>	L9 and outpput adj signal\$1	0	<u>L10</u>
<u>L9</u>	L8 and voltage adj level\$1 and chang\$3	12	<u>L9</u>
<u>L8</u>	L3 and voltage adj level\$1	12	<u>L8</u>
<u>L7</u>	L6 and bus adj compress\$3	0	<u>L7</u>
<u>L6</u>	voltage adj conver\$4	18360	<u>L6</u>
<u>L5</u>	L3 and voltage adj conver\$4	0	<u>L5</u>
<u>L4</u>	L3 and voltage adj converter\$3	0	<u>L4</u>
<u>L3</u>	bus adj compress\$3	97	<u>L3</u>
<u>L2</u>	L1 and voltage adj convert\$3	0	<u>L2</u>
<u>L1</u>	bus adj compressing	9	<u>L1</u>

END OF SEARCH HISTORY

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L11</u>	L10 and bus same compression	0	<u>L11</u>
<u>L10</u>	L9 and bus	25	<u>L10</u>
<u>L9</u>	L6 and controller	52	<u>L9</u>
<u>L8</u>	L6 and n adj voltage adj converters	0	<u>L8</u>
<u>L7</u>	L6 and bus adj compression	0	<u>L7</u>
<u>L6</u>	L5 and compression	126	<u>L6</u>
<u>L5</u>	L4 and output adj signal	1995	<u>L5</u>
<u>L4</u>	L3 and voltage adj level\$1	3149	<u>L4</u>
<u>L3</u>	voltage adj converter\$3	10325	<u>L3</u>
<u>L2</u>	L1 and voltage adj converter\$3	0	<u>L2</u>
<u>L1</u>	bus adj compress\$3	97	<u>L1</u>

END OF SEARCH HISTORY

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L9</u>	L8 and quantizing same analog adj signal\$1	10	<u>L9</u>
<u>L8</u>	L7 and analog adj signal	265	<u>L8</u>
<u>L7</u>	L1 and quantiz\$4 and cod\$3	884	<u>L7</u>
<u>L6</u>	L5 and analog adj signal	2	<u>L6</u>
<u>L5</u>	L2 and quantiz\$4 and cod\$3	6	<u>L5</u>
<u>L4</u>	L3 and analog adj signal\$1	12	<u>L4</u>
<u>L3</u>	L2 and (quantiz\$4 or cod\$3)	27	<u>L3</u>
<u>L2</u>	bus adj decompress\$3	36	<u>L2</u>
<u>L1</u>	bus and decompress\$3	5072	<u>L1</u>

END OF SEARCH HISTORY

Set Name Query

side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L28</u>	L26 and second adj voltage adj control\$3	35	<u>L28</u>
<u>L27</u>	L26 and first adj voltage adj control\$3	33	<u>L27</u>
<u>L26</u>	L23 and first adj resistor and second adj resistor	1339	<u>L26</u>
<u>L25</u>	L23 and first adj resistor	1494	<u>L25</u>
<u>L24</u>	l23 and first adj voltage adj control adj means	0	<u>L24</u>
<u>L23</u>	l1 and voltage adj control and resistor	16814	<u>L23</u>
<u>L22</u>	l1 and adder near5 voltage adj level\$1 and analog adj signal\$1	0	<u>L22</u>
<u>L21</u>	l1 and adder near5 voltage adj level\$1	5	<u>L21</u>
<u>L20</u>	l1 and adder same voltage adj level\$1	70	<u>L20</u>
<u>L19</u>	L1 and voltage adj control\$3 adj output and analog adj signal and adder	5	<u>L19</u>
<u>L18</u>	L1 and voltage adj control\$3 adj output same analog adj signal	1	<u>L18</u>
<u>L17</u>	L1 and voltage adj control\$3 adj output and analog adj signal	70	<u>L17</u>
<u>L16</u>	L1 and voltage adj control\$3 adj output near5 analog adj signal	0	<u>L16</u>
<u>L15</u>	L1 and voltage adj control\$3 adj output	459	<u>L15</u>
<u>L14</u>	L13	16	<u>L14</u>
<u>L13</u>	L12 and output	16	<u>L13</u>
<u>L12</u>	l2 and Voltage adj control near2 bit adj line\$1	17	<u>L12</u>
<u>L11</u>	l2 and Voltage adj control near3 bit adj line\$1	37	<u>L11</u>
<u>L10</u>	l2 and Voltage adj control near5 bit adj line\$1	61	<u>L10</u>
<u>L9</u>	l2 and Voltage adj control near5 (bit or data) adj line\$1	69	<u>L9</u>
<u>L8</u>	L4 and bus adj lines	31	<u>L8</u>
<u>L7</u>	L4 and bus and decompress\$3	1	<u>L7</u>
<u>L6</u>	L5 and bus and decompress\$3	0	<u>L6</u>
<u>L5</u>	L4 and voltage adj control adj means	0	<u>L5</u>
<u>L4</u>	l2 and Voltage adj control same bit adj line\$1	555	<u>L4</u>
<u>L3</u>	l2 and Voltage adj control same (bit or data) adj line\$1	721	<u>L3</u>
<u>L2</u>	L1 and (bit or data) adj line\$1	3277	<u>L2</u>
<u>L1</u>	voltage adj control	30111	<u>L1</u>

END OF SEARCH HISTORY